

# NT1191GEAE3S GNSS Wideband Low Noise Amplifier

#### **FEATURES**

Frequency range: 1164 MHz to 1610 MHz
Supply voltage: 1.5 V to 5.5 V (3.3 V typ.)

Current consumption: 5.5 mA typ.

Gain: 17.5 dB typ. @ L1/2/5/6 band
NF: 0.75 dB typ. @ L1/2/5/6 band
P-1dB(IN): -10 dBm typ. @ L1/2/5/6 band
IIP3: 0 dBm typ. @ L1/2/5/6 band

With stand-by function

Package size: 1.6 x 1.6 x 0.78 mm typ.
RoHS compliant and Halogen Free, MSL1

#### APPLICATIONS

- GNSS (GPS, GLONASS, Galileo, BeiDou, etc.) receiver application
- Multi-GNSS application for high-precision positioning
- Active antenna, car navigation, dashboard camera, GNSS tracker, drone

#### **GENERAL DESCRIPTION**

The NT1191 is a GNSS wideband low noise amplifier (LNA) designed for multi-GNSS receiver applications.

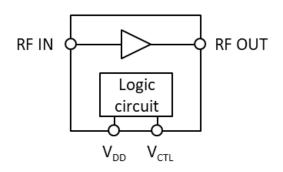
This LNA features good gain flatness and low noise figure (NF) in the entire GNSS band from 1164 MHz to 1610 MHz. It supports a wide range of supply voltage from 1.5 V to 5.5 V. The stand-by function contributes to reduce current consumption. Integrated ESD protection device on each port is achieved excellent ESD robustness. The wide operating temperature range from -40 to +105 °C.

Only one external input matching inductor is required. The package is 1.6 x 1.6 mm in size with wettable flank, and corresponds to automated optical inspection (AOI).



DFN1616-6-GE 1.6 × 1.6 × 0.78 (mm)

#### **BLOCK DIAGRAM**





#### **■ PRODUCT NAME INFORMATION**

### NT1191 GE A E3 S

Description of configuration

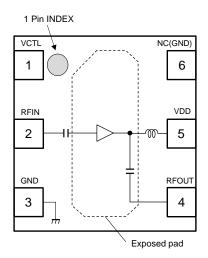
Suffix	Item	Description
GE	Package code	Indicates the package. Refer to the order information.
Α	Version	Indicates the product version. "A" is initial version.
E3	Packing	Refer to the packing specifications.
S	Grade	Indicates the quality grade. "S" means general-purpose and consumer application.  Operating temperature range: −40°C to +105°C, Test temperature: +25°C

#### **■ ORDER INFORMATION**

PRODUCT NAME	PACKAGE	RoHS	HALOGEN- FREE	PLATING COMPOSITION	MARKING	WEIGHT (mg)	Quantity per Reel (pcs)
NT1191GEAE3S	DFN1616-6-GE	Yes	Yes	SnBi	1191	5.4	3000



#### **■ PIN DESCRIPTIONS**



**DFN1616-6-GE Pin Configuration** 

Pin No.	Pin Name	Description
1	VCTL	Control signal input terminal
2	RFIN	RF input terminal
3	GND	Ground terminal
4	RFOUT	RF output terminal
5	VDD	Operating voltage supply terminal
6	NC(GND)	No connected terminal (connect to ground)
-	Exposed pad	Ground terminal

Please refer to "APPLICATION CIRCUIT" for details.

#### **■ TRUTH TABLE**

"H"= $V_{CTL}(H)$ , "L"= $V_{CTL}(L)$ 

Vctl	Mode
Н	Active mode
L	Stand-by mode



#### ■ ABSOLUTE MAXIMUM RATINGS

General conditions:  $T_a = +25^{\circ}C$ ,  $Z_s = Z_l = 50\Omega$ 

Parameter	Symbol	Ratings	Unit
Supply Voltage	$V_{DD}$	6.0	V
Control Voltage	Vctl	6.0	V
Input Power	P <sub>IN</sub> *1	+15	dBm
Power Dissipation	P <sub>D</sub> *2	1100	mW
Operating Temperature Range	T <sub>opr</sub>	-40 to +105	°C
Storage Temperature Range	T <sub>stg</sub>	−55 to +150	°C

 $<sup>^{*1}</sup>$   $V_{DD} = 3.3 \ V$ 

#### **ABSOLUTE MAXIMUM RATINGS**

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause permanent damage and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

#### **■ THERMAL CHARACTERISTICS**

Parameter	Value
Thermal Resistance (θja)	θja = 116°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 43°C/W

 $\theta ja$  : Junction-to-Ambient Thermal Resistance

ψjt: Junction-to-Top Thermal Characterization Parameter

#### ■ ELECTROSTATIC DISCHARGE (ESD) PROTECTION VOLTAGE

Symbol	Conditions	Protection Voltage
НВМ	$C = 100 \text{ pF}, R = 1.5 \text{ k}\Omega$	±2000 V
CDM	Direct CDM	±1000 V

#### **ESD PROTECTION VOLTAGE**

The electrostatic discharge test is done based on JEITA ED-4701. In the HBM method, ESD is applied using the GND pin as reference pins.



 $<sup>^{*2}</sup>$  4-layer FR4 PCB with through-hole (101.5 x 114.5 mm),  $T_j = 150$ °C

#### **■ RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	Value	Unit
Supply Voltage	$V_{DD}$	1.5 to 5.5	V
Control Voltage	Vctl	1.5 to 5.5	V
Operating Temperature Range	Ta	-40 to +105	°C

#### RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

#### ■ ELECTRICAL CHARACTERISTICS 1 (DC)

General conditions:  $T_a = +25$ °C,  $Z_s = Z_l = 50\Omega$ 

Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit
Supply Voltage	V <sub>DD</sub>		1.5	3.3	5.5	V
Control Voltage (High)	V <sub>CTL</sub> (H)		1.5	1.8	5.5	V
Control Voltage (Low)	V <sub>CTL</sub> (L)		0	0	0.3	V
Operating Current	lob	RF OFF, V <sub>DD</sub> = 3.3 V, V <sub>CTL</sub> = 1.8 V	-	5.5	8.0	A
		RF OFF, V <sub>DD</sub> = 1.8 V, V <sub>CTL</sub> = 1.8 V	-	3.5	-	mA
		RF OFF, V <sub>DD</sub> = 3.3 V, V <sub>CTL</sub> = 0 V	-	0.1	3.0	
		RF OFF, V <sub>DD</sub> = 1.8 V, V <sub>CTL</sub> = 0 V	-	0.1	-	μΑ
Control Current	I <sub>CTL</sub>	RF OFF, V <sub>CTL</sub> = 1.8 V	-	5	20	μА



#### **■ ELECTRICAL CHARACTERISTICS 2 (RF)**

General conditions:  $V_{DD} = 3.3 \text{ V}$ ,  $V_{CTL} = 1.8 \text{V}$ , f = 1164 MHz to 1610 MHz,  $T_a = +25 ^{\circ}\text{C}$ ,  $Z_s = Z_l = 50 \Omega$ , with application circuit

Parameter	Symbol	= 1.8V, f = 1164 MHz to 1610 MHz, T <sub>a</sub> = +25°C, 2 Conditions	MIN	TYP	MAX	Unit
Small Signal Gain Gain		f = 1176 MHz (L5 band), Exclude PCB, connector loss*3  f = 1227 MHz (L2 band), Exclude PCB, connector loss*3  f = 1278 MHz (L6 band), Exclude PCB, connector loss*3  f = 1575 MHz (L1 band), Exclude PCB, connector loss*3	15.0	17.5	19.0	dB
Noise Figure NF		f = 1176 MHz (L5 band), Exclude PCB, connector loss*4 f = 1227 MHz (L2 band), Exclude PCB, connector loss*4 f = 1278 MHz (L6 band), Exclude PCB, connector loss*4 f = 1575 MHz (L1 band), Exclude PCB, connector loss*4		0.75	1.1	dB
Isolation	ISL	f = 1176 MHz (L5 band) f = 1227 MHz (L2 band) f = 1278 MHz (L6 band) f = 1575 MHz (L1 band)	32	35	-	dB
Input Power at 1 dB Gain Compression Point	P-1dB(IN)	f = 1176 MHz (L5 band) f = 1227 MHz (L2 band) f = 1278 MHz (L6 band) f = 1575 MHz (L1 band)	15.0	-10.0	-	dBm
IIP3   f1 = 1176 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   f1 = 1227 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   f1 = 1278 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   f1 = 1278 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   f1 = 1575 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   f1 = 1575 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   f1 = 1575 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   f1 = 1575 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   f1 = 1575 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   f1 = 1575 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   Pin = -30 dBm   f1 = 1575 MHz, f2 = f1 + 1 MHz,   Pin = -30 dBm   Pin = -30 dBm		Pin = -30 dBm f1 = 1227 MHz, f2 = f1 + 1 MHz, Pin = -30 dBm f1 = 1278 MHz, f2 = f1 + 1 MHz, Pin = -30 dBm	5.0	0	-	dBm
RF IN Return Loss	RLi	f = 1176 MHz (L5 band) f = 1227 MHz (L2 band) f = 1278 MHz (L6 band) f = 1575 MHz (L1 band)	6.0	12.0	-	dB
RF OUT Return Loss $ RLo = \begin{cases} f = 1176 \text{ MHz (L5 b)} \\ f = 1227 \text{ MHz (L6 b)} \\ f = 1278 \text{ MHz (L6 b)} \end{cases} $		f = 1176 MHz (L5 band) f = 1227 MHz (L2 band) f = 1278 MHz (L6 band) f = 1575 MHz (L1 band)	10.0	18.0	-	dB
k factor	k	f = 50 MHz to 10 GHz	1.0	-	-	-

<sup>\*3</sup> PCB and connector losses of input and output: 0.09 dB (1176 MHz), 0.10 dB (1227 MHz), 0.11 dB (1278 MHz), 0.14 dB (1575 MHz)



<sup>\*4</sup> PCB and connector losses of input: 0.04 dB (1176 MHz, 1227 MHz), 0.05 dB (1278 MHz), 0.06 dB (1575 MHz)

■ ELECTRICAL CHARACTERISTICS 3 (RF)

General conditions:  $V_{DD} = 1.8 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ , f = 1164 MHz to 1610 MHz,  $T_a = +25 ^{\circ}\text{C}$ ,  $Z_s = Z_l = 50 \Omega$ , with application circuit

Parameter	Symbol	= 1.8 V, f = 1164 MHz to 1610 MHz, T <sub>a</sub> = +25°C, Conditions	MIN	TYP	MAX	Unit
Small Signal Gain	Gain	f = 1176 MHz (L5 band), Exclude PCB, connector loss*3  f = 1227 MHz (L2 band), Exclude PCB, connector loss*3  f = 1278 MHz (L6 band), Exclude PCB, connector loss*3  f = 1575 MHz (L1 band), Exclude PCB, connector loss*3	- - -	16.0	-	dB
Noise Figure	f = 1176 MHz (L5 band), Exclude PCB, connector loss*4  f = 1227 MHz (L2 band), Exclude PCB, connector loss*4  f = 1278 MHz (L6 band), Exclude PCB, connector loss*4  f = 1575 MHz (L1 band), Exclude PCB, connector loss*4			0.85	-	dB
Isolation	ISL	f = 1176 MHz (L5 band) f = 1227 MHz (L2 band) f = 1278 MHz (L6 band) f = 1575 MHz (L1 band)		34	-	dB
Input Power at 1 dB Gain Compression Point	P-1dB(IN)	f = 1176 MHz (L5 band) f = 1227 MHz (L2 band) f = 1278 MHz (L6 band) f = 1575 MHz (L1 band)	- - -	-16.0	-	dBm
Input 3rd Order Intercept Point	IIP3	f1 = 1176 MHz, f2 = f1 + 1 MHz, Pin = -30 dBm f1 = 1227 MHz, f2 = f1 + 1 MHz, Pin = -30 dBm f1 = 1278 MHz, f2 = f1 + 1 MHz, Pin = -30 dBm f1 = 1575 MHz, f2 = f1 + 1 MHz, Pin = -30 dBm		-6.0	-	dBm
RF IN Return Loss	RLi	f = 1176 MHz (L5 band) f = 1227 MHz (L2 band) f = 1278 MHz (L6 band) f = 1575 MHz (L1 band)	- - -	10.0	-	dB
RF OUT Return Loss	RLo	f = 1176 MHz (L5 band) f = 1227 MHz (L2 band) f = 1278 MHz (L6 band) f = 1575 MHz (L1 band)	-	15.0	-	dB
k factor	k	f = 50 MHz to 10 GHz	1.0	-	-	-

<sup>\*3</sup> PCB and connector losses of input and output: 0.09 dB (1176 MHz), 0.10 dB (1227 MHz), 0.11 dB (1278 MHz), 0.14 dB (1575 MHz)

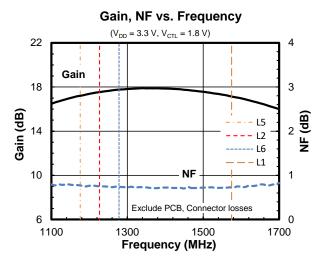


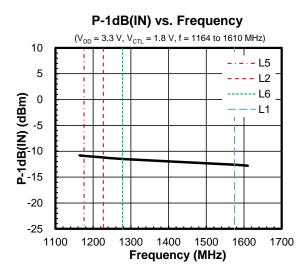
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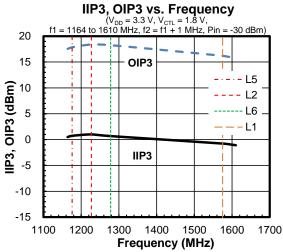
PRELIMINARY SPECIFICATIONS SUBJECT TO CHANGE

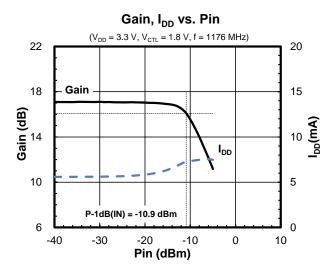
#### **■ TYPICAL CHARACTERISTICS**

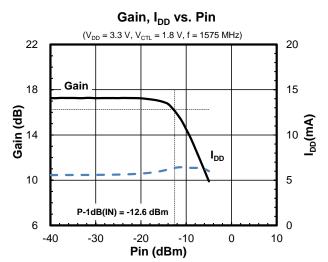
Conditions:  $V_{DD} = 3.3 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ ,  $T_a = +25^{\circ}\text{C}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)



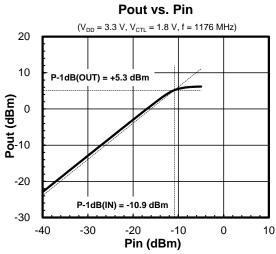


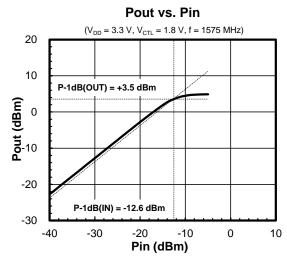


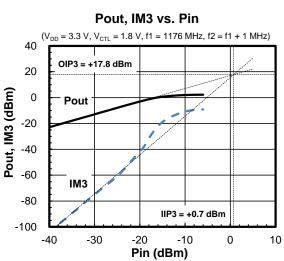


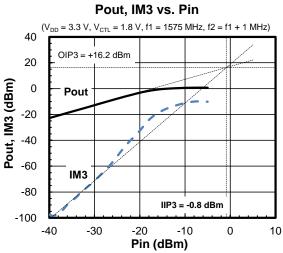


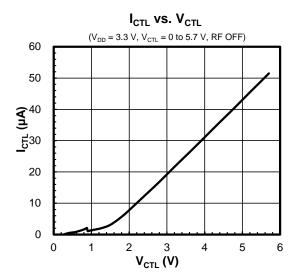
Conditions:  $V_{DD} = 3.3 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ ,  $T_a = +25^{\circ}\text{C}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)



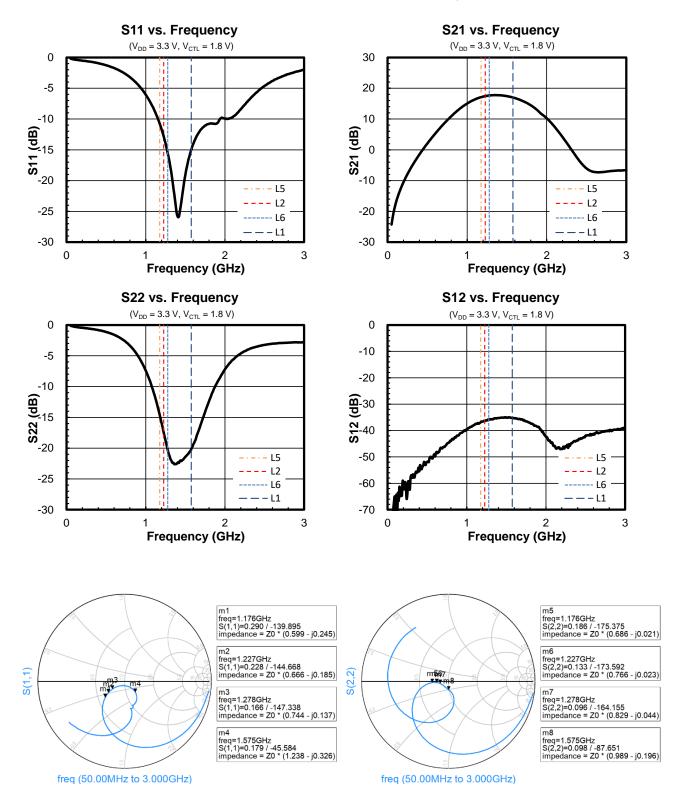




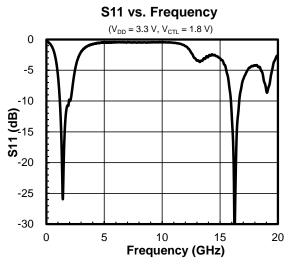


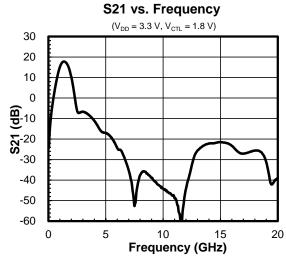


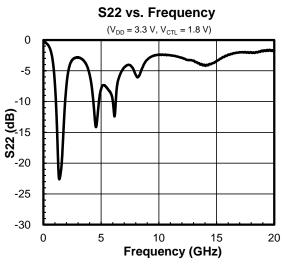
Conditions:  $V_{DD} = 3.3 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ , f = 50 MHz to 3 GHz,  $T_a = +25^{\circ}\text{C}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)

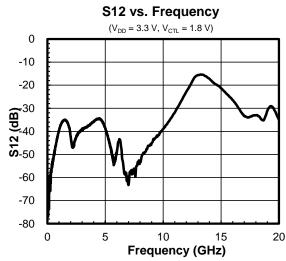


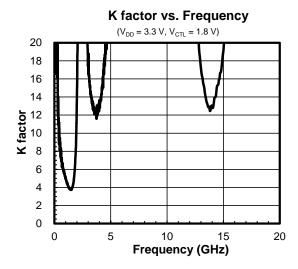
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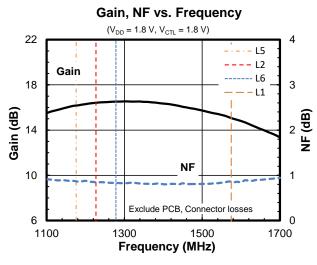


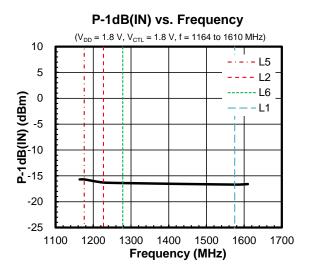


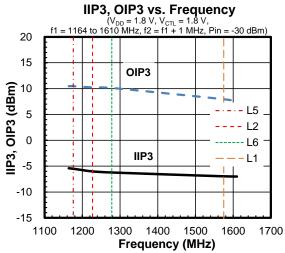


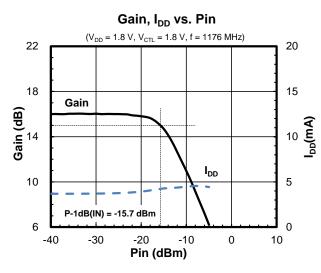


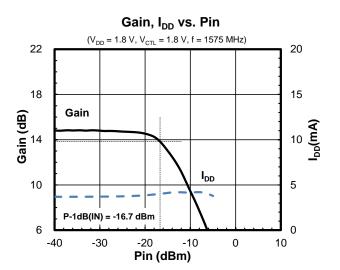
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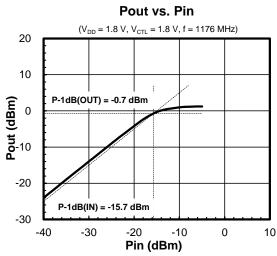


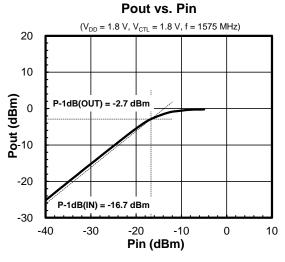


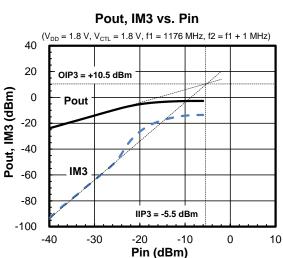


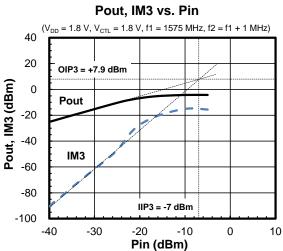


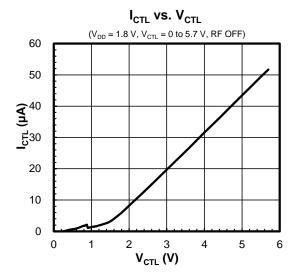
Conditions:  $V_{DD} = 1.8 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ ,  $T_a = +25^{\circ}\text{C}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)



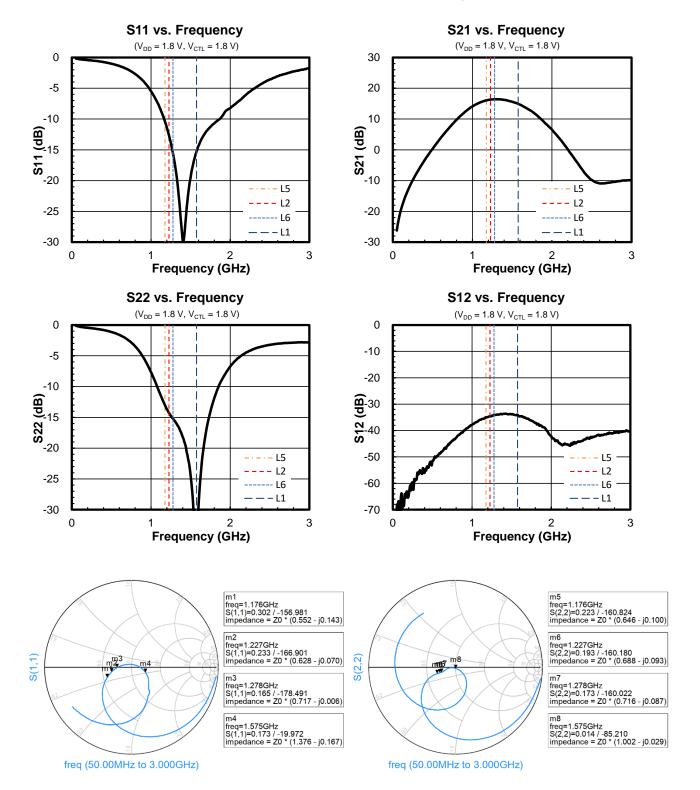




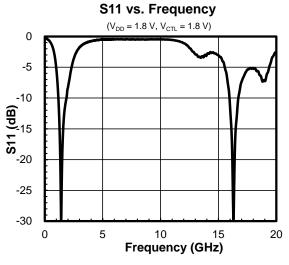


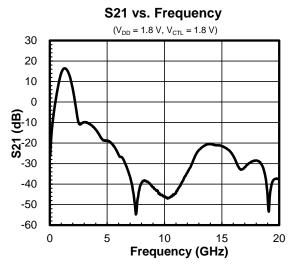


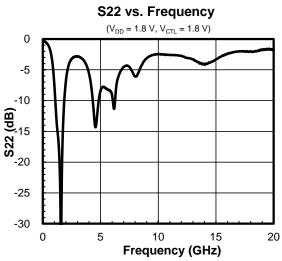
Conditions:  $V_{DD} = 1.8 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ , f = 50 MHz to 3 GHz,  $T_a = +25^{\circ}\text{C}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)

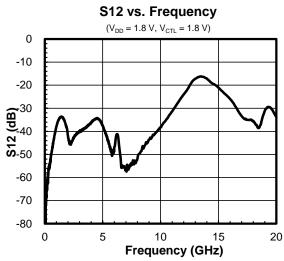


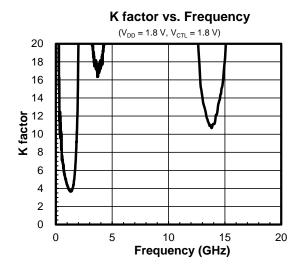
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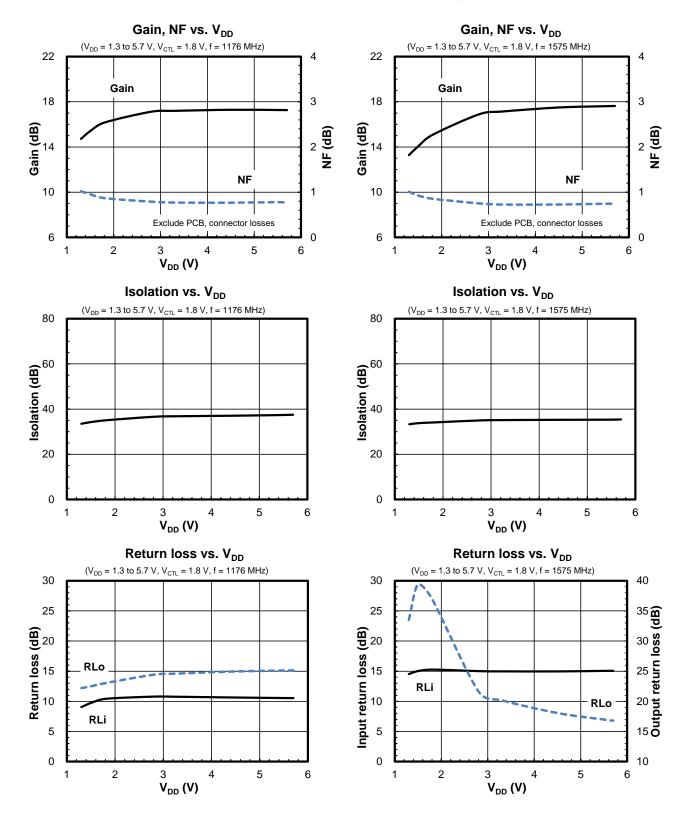




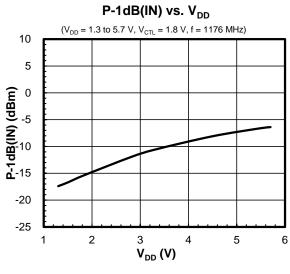


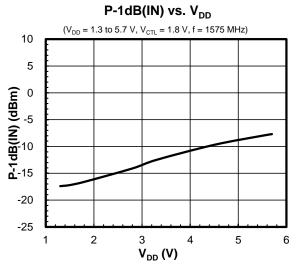


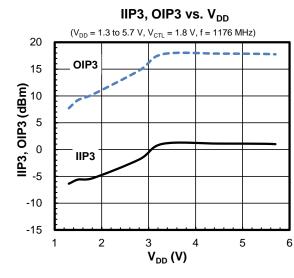
Conditions:  $V_{CTL} = 1.8 \text{ V}$ ,  $T_a = +25^{\circ}\text{C}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)

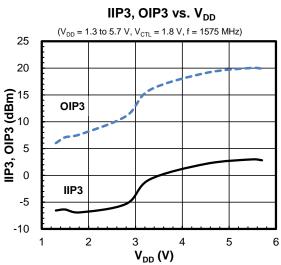


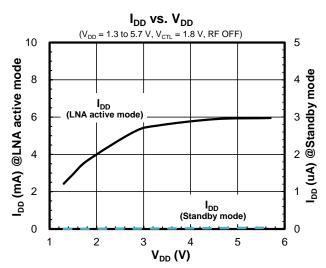
Conditions:  $V_{CTL} = 1.8 \text{ V}$ ,  $T_a = +25^{\circ}\text{C}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)



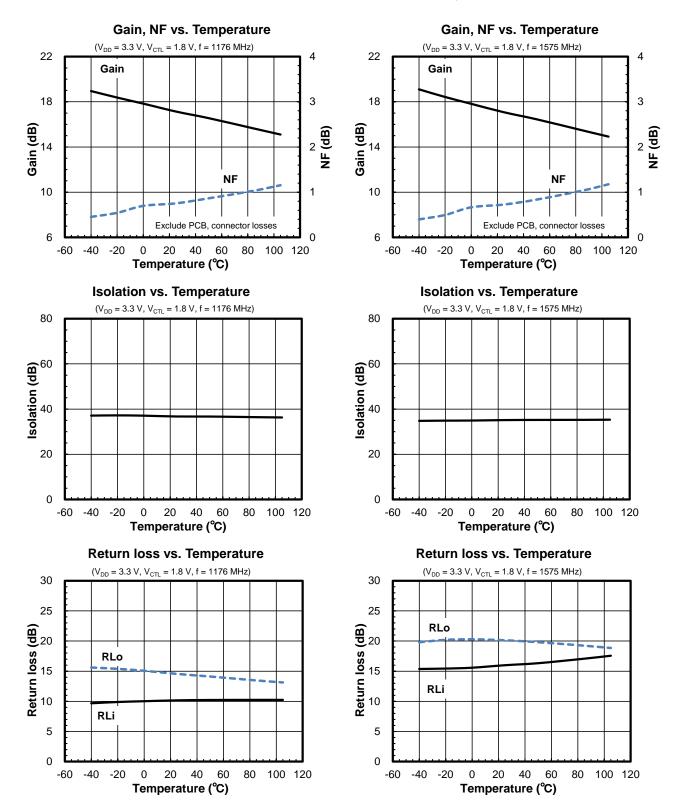




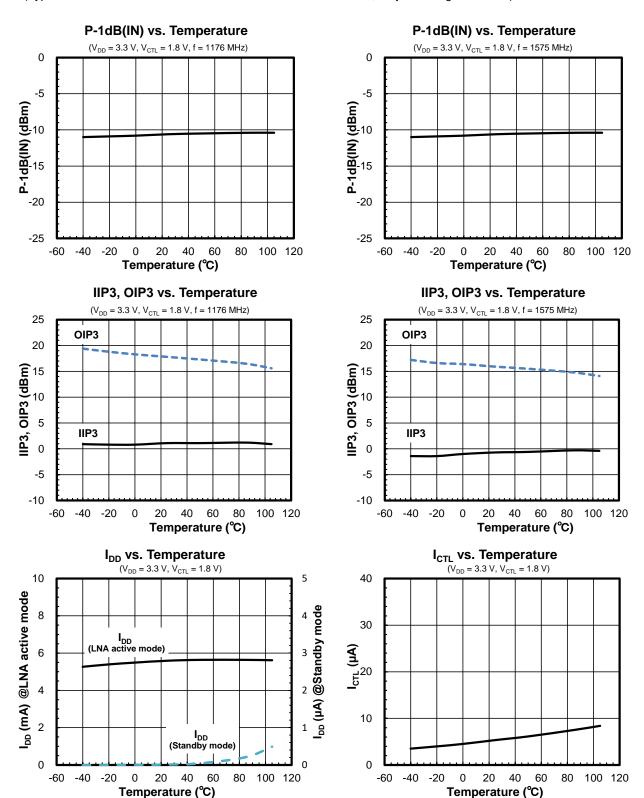




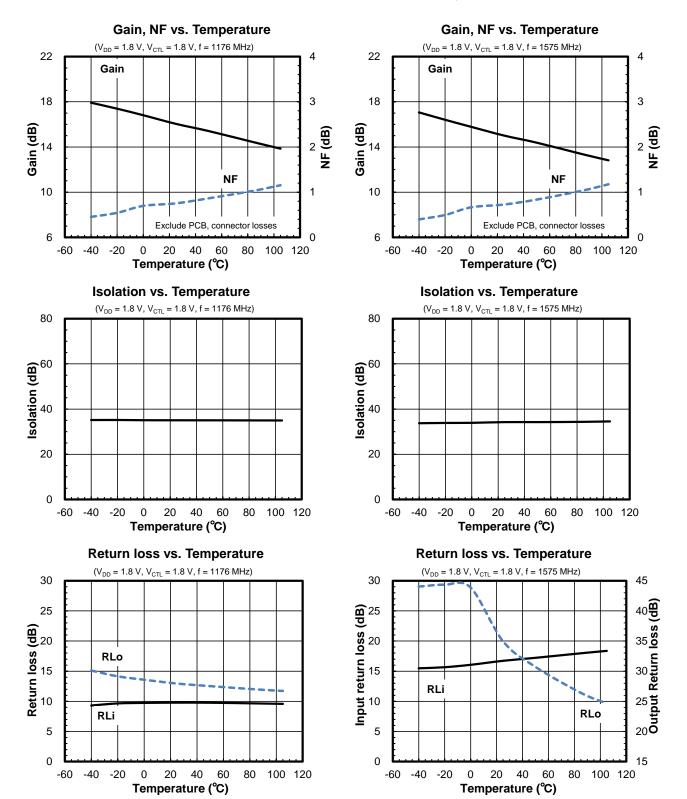
Conditions:  $V_{DD} = 3.3 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)



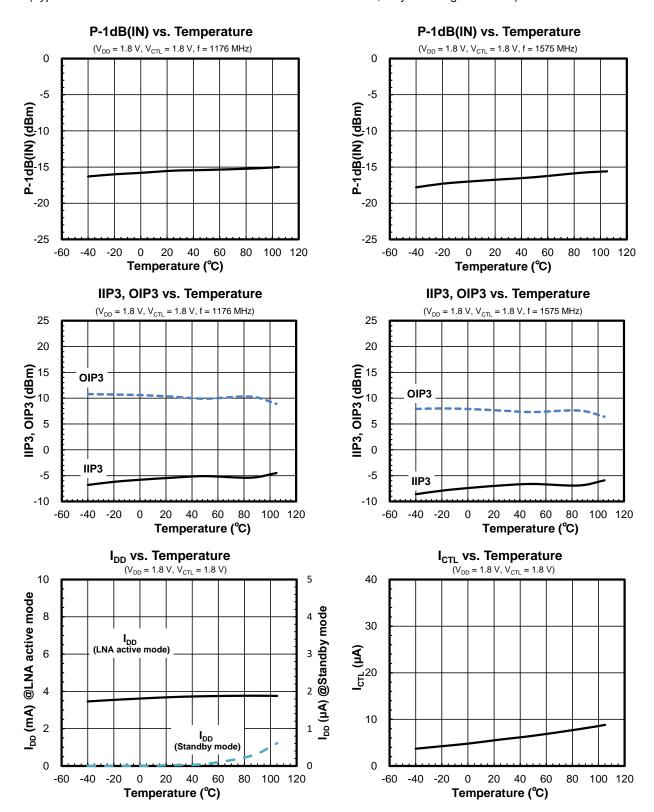
Conditions:  $V_{DD} = 3.3 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)



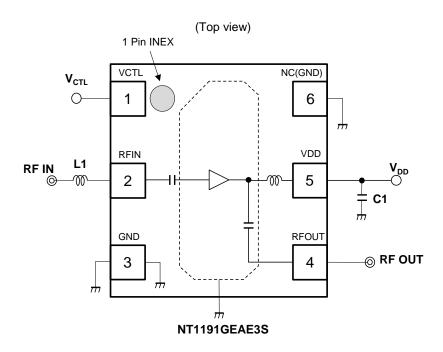
Conditions:  $V_{DD} = 1.8 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)



Conditions:  $V_{DD} = 1.8 \text{ V}$ ,  $V_{CTL} = 1.8 \text{ V}$ ,  $Z_s = Z_l = 50\Omega$ , with application circuit (Typical characteristics are intended to be used as reference data, they are not guaranteed.)



#### **■ APPLICATION CIRCUIT**



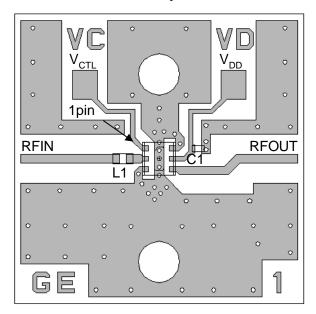
#### <Parts list>

Part ID	Value	Notes
L1	9.5 nH	LQW15AN_00 series (MURATA)
C1	1000 pF	GRM03 series (MURATA)



#### **■ APPLICATION NOTES**

#### • Evaluation Board / PCB layout



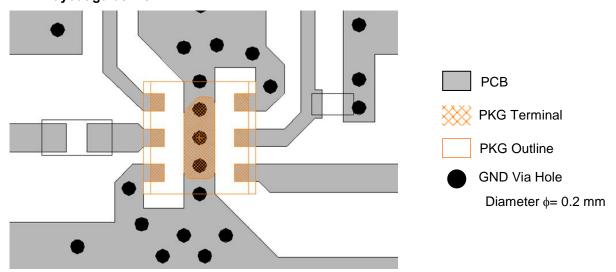
#### **PCB**

Substrate: FR-4 Thickness: 0.2 mm

Microstrip line width: 0.4 mm ( $Z_0 = 50\Omega$ )

Size: 14.0 x 14.0 mm

#### <PCB layout guideline>



#### **PRECAUTIONS**

- All external parts should be placed as close as possible to the LNA.
- For good RF performance, all GND terminals must be connected to PCB ground plane of substrate, and via-holes for GND should be placed near the LNA.



#### • NF Measurement Block Diagram

#### **Measuring instruments**

NF Analyzer : Keysight N8973A Noise Source : Keysight N4000A

#### Setting the NF analyzer

Measurement mode form

Device under test : Amplifier

System downconverter : off

Mode setup form

Sideband : LSB

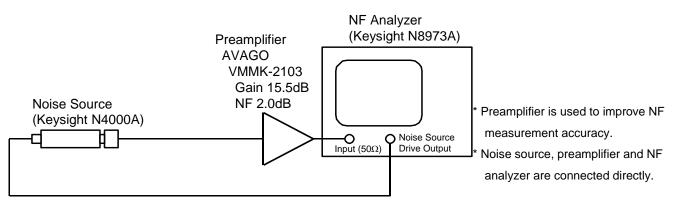
Averages : 8

Average mode : Point

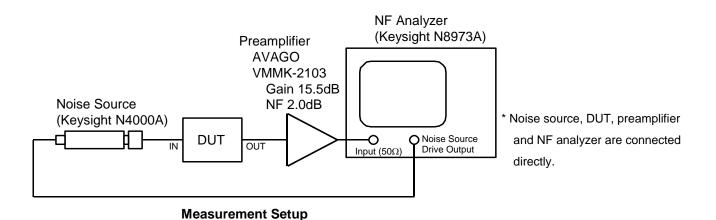
Bandwidth : 4 MHz

Loss comp : off

Tcold : setting the temperature of noise source (Auto)



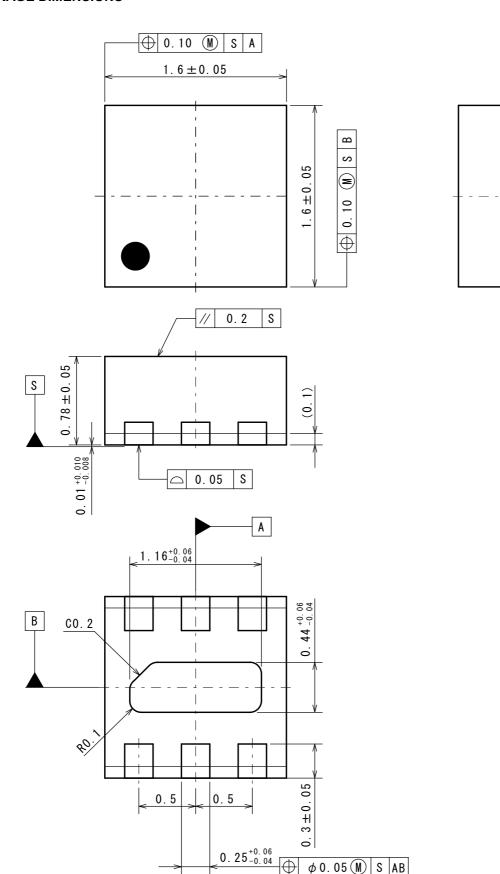
#### **Calibration setup**





#### **■ PACKAGE DIMENSIONS**

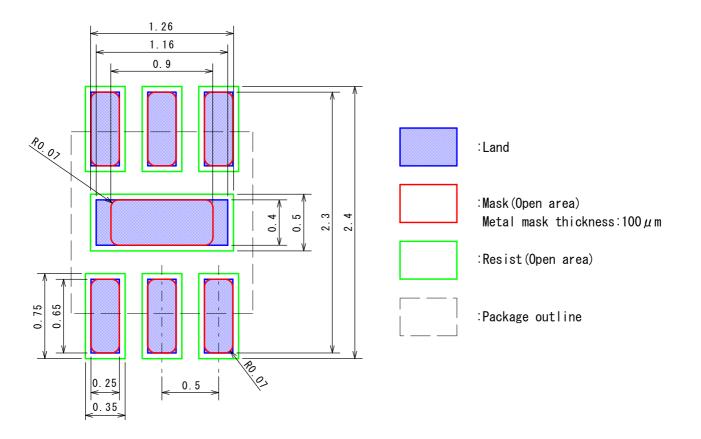
UNIT: mm





#### **■ EXAMPLE OF SOLDER PADS DIMENSIONS**

UNIT: mm

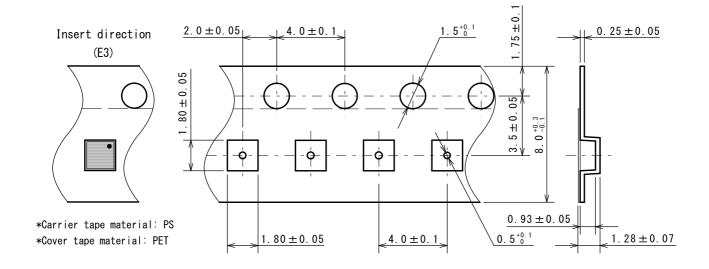


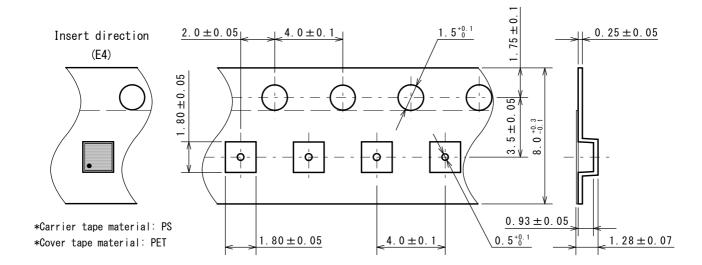


#### **■ PACKING SPEC**

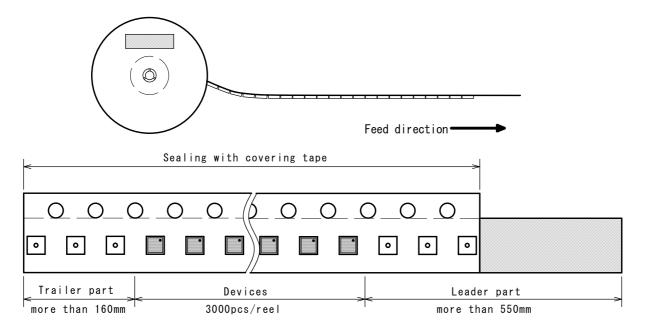
UNIT: mm

#### (1)Taping dimensions / Insert direction

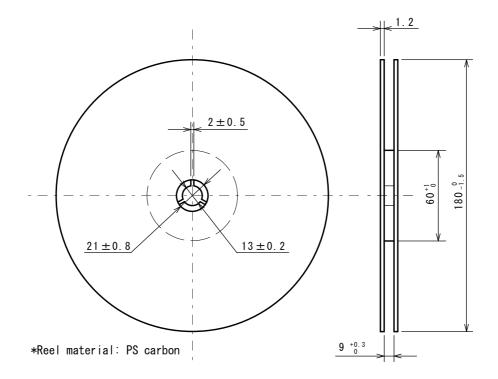




#### (2) Taping state



#### (3) Reel dimensions





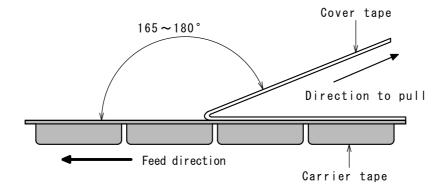
**DFN1616-6-GE**Ver. PI-DFN1616-6-GE-E-A

#### (4) Peeling strength

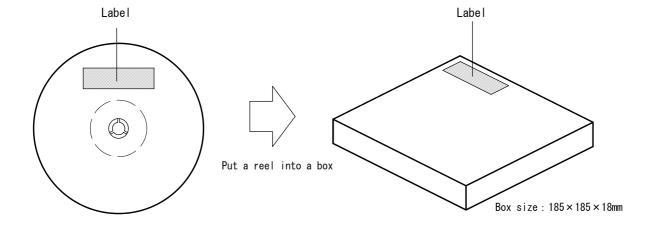
Peeling strength of cover tape

•Peeling angle 165~180° degrees to the taped surface.

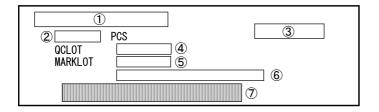
•Peeling speed 300mm/min •Peeling strength 0.1 ~ 0.7N



#### (5) Packing state



#### (6) Label



1	Product name
2	Quantity
3	Product code
4	QC LOT No.
<b>⑤</b>	MARK LOT No.
6	Environmental notation
(7)	Barcode



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  - 8-2. Quality Warranty Remedies
    - When it has been proved defective due to manufacturing factors as a result of defect analysis by us, we will either deliver a substitute for the defective product or refund the purchase price of the defective product.
    - Note that such delivery or refund is sole and exclusive remedies to your company for the defective product.
  - 8-3. Remedies after Quality Warranty Period
    - With respect to any defect of this product found after the quality warranty period, the defect will be analyzed by us. On the basis of the defect analysis results, the scope and amounts of damage shall be determined by mutual agreement of both parties. Then we will deal with upper limit in Section 8-2. This provision is not intended to limit any legal rights of your company.
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